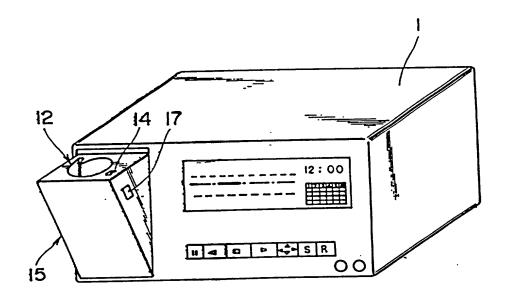
the first three other than that that it there is next to the the three t

2.0

FIG 2



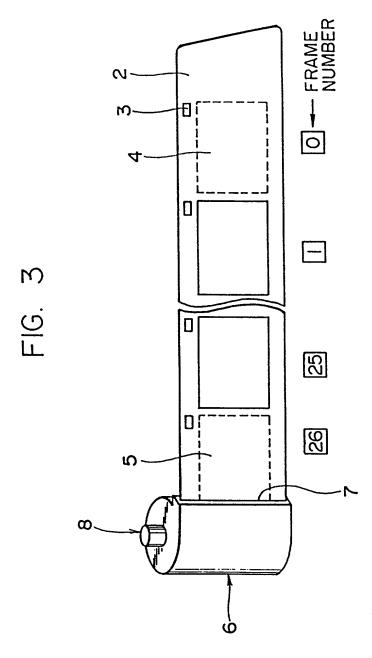


FIG. 4

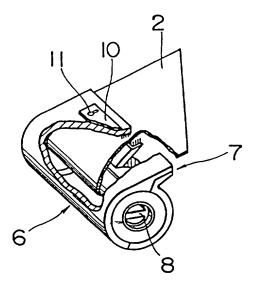
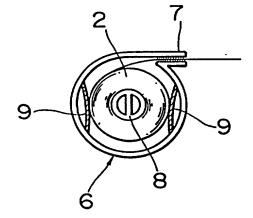
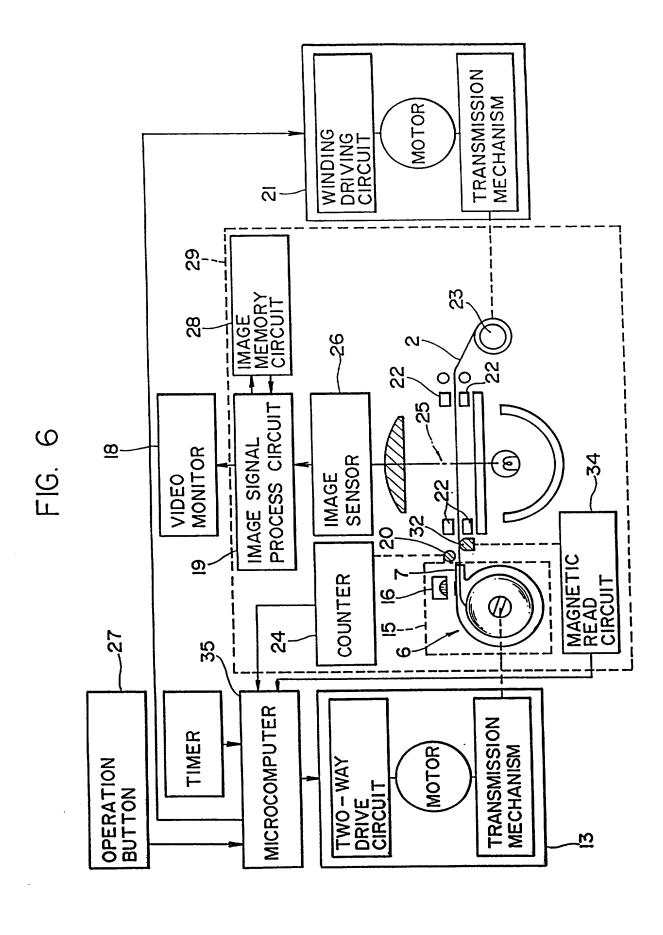


FIG. 5





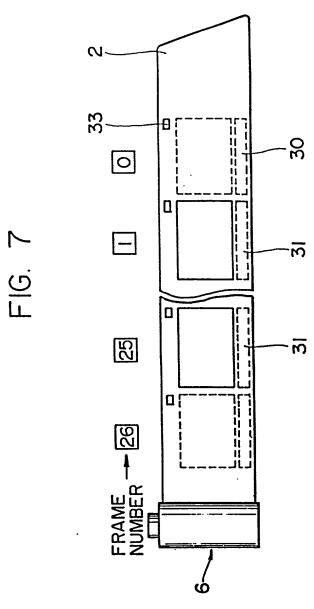


FIG. 8

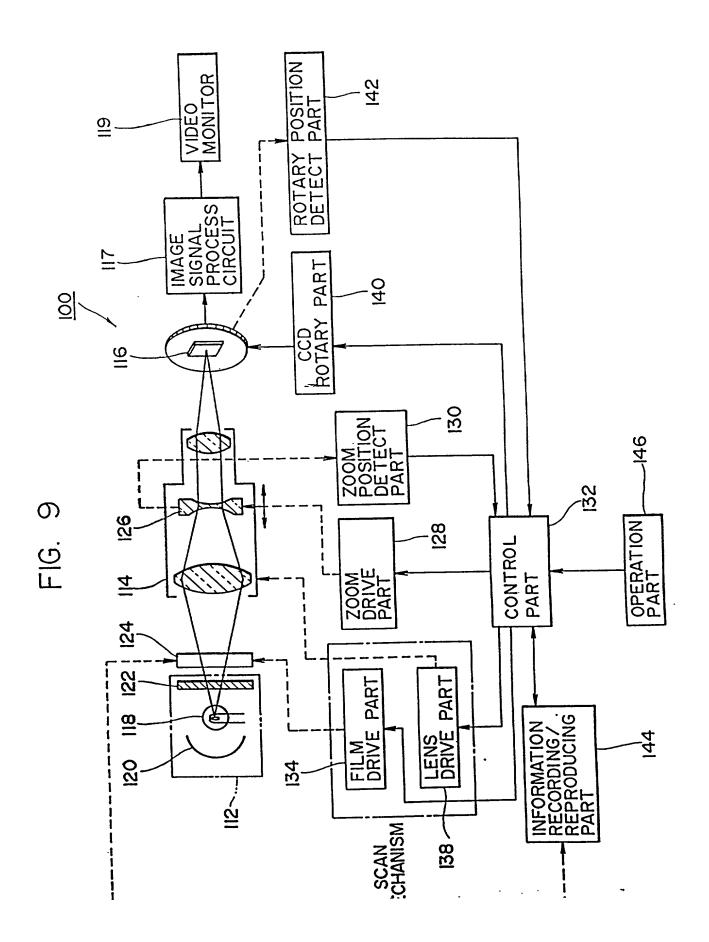
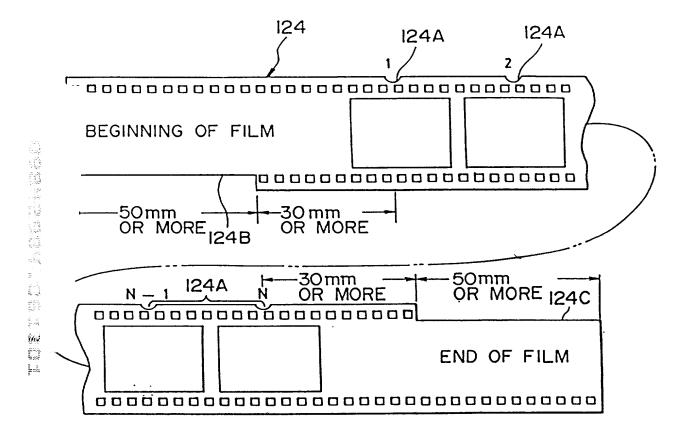
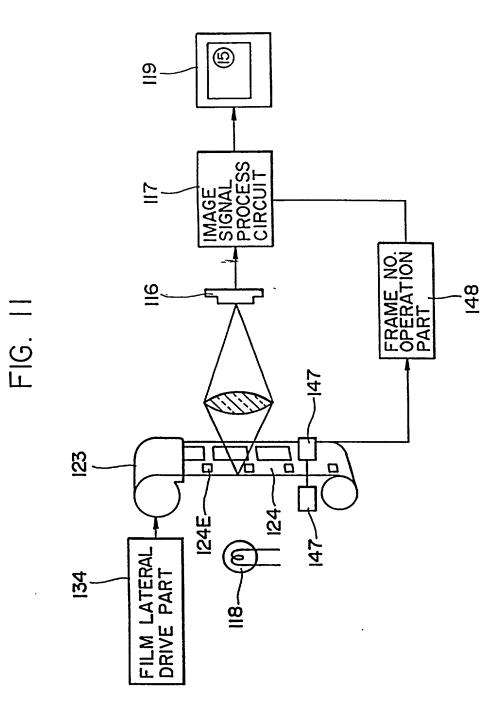
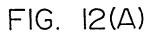


FIG. 10



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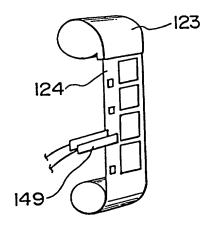
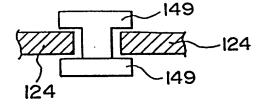


FIG. 12(B)

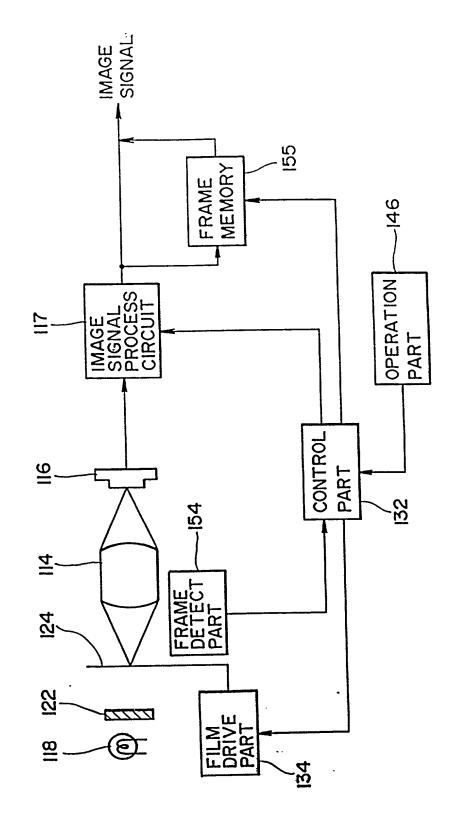


MONITOR <u>€</u> IMAGE SIGNAL PRØCESS CIRCUIT CONTROL CIRCUIT 152 <u>-</u> 9 FRAME NO. OPERATION PART CIRCUIT <u>4</u>8 VOICE 120 ሐ 147~

FIG. 14

IMAGE SIGNAL | OPERATION | PART 146 FRAME MEMORY 153 IMAGE SIGNAL PROCESS CIRCUIT <u>| 1</u> CONTROL 9 **PART** 132 -154 FRAME DETECT PART 122 FILM DRIVE PART 134

F1G. 15



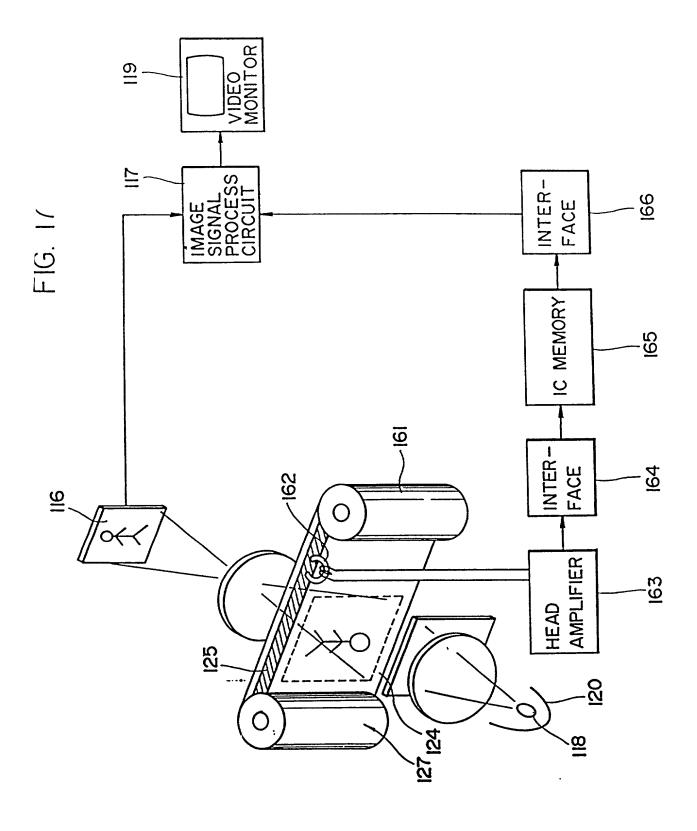


130 -1)EXPOSURE -2)CORRECTION DIAL ZOOM POSITION DETECT PART SHUTTER SPEED SETTING DIAL -156 EXPOSURE CORRECTION SHUTTER F16. 1/60 PRESENCE OR ABSENCE OF STROBE STROBE UP/Down IC MEMORY 133C 135 133A-TIME 90.9.14 . 30 RIC F NUMBER DATE 4 133B-INTER-133D FACE 159 157 AMPLIFIER 58 HEAD <u>124</u> 125 127

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SPEAKER MICRO-PHONE 165C 165E 165D -165F 165B A/D D/A CONTROL MEMORY CIRCUIT 165A AMP 163 127A REPRODUCING 124B 162A 127 124A RECORDING <u>124</u> 162B 127B

FIG. 18

FIG. 19

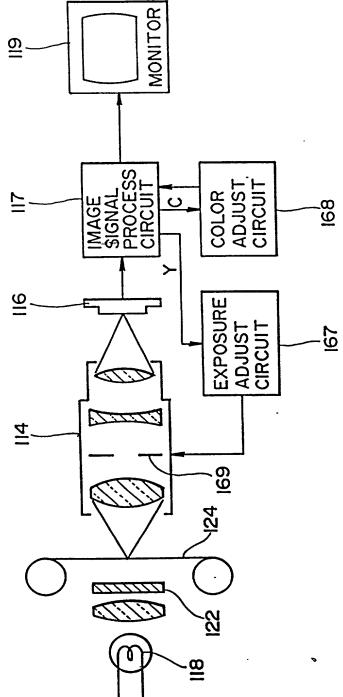


FIG. 20

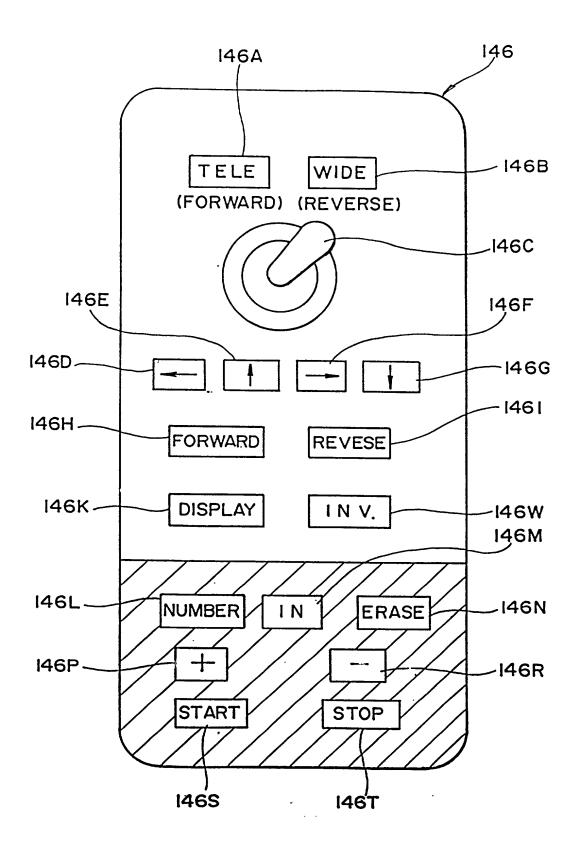


FIG. 21 IMAGE IS 170 DISPLAYED REPRODUCTION -172 IMAGE IS SET DISPLAY TIME IS -174 SET REPRODUCTION -176 INFORMATION IS INPUT 178 NO IF INPUT IS COMPLETED YES 180 DISPLAY IMAGE IS **AUTOMATICALLY** REPRODUCED 186 184 -182 YES IF START YES IMAGE IS IF STOP SWITCH IS TURNED SWITCH IS TURNED STOPPED ON ? ON ? NO NO NO IF IT IS FINAL FRAME 188 YES **AUTOMATIC REPRODUCTION** 189 IS COMPLETED

FIG. 22

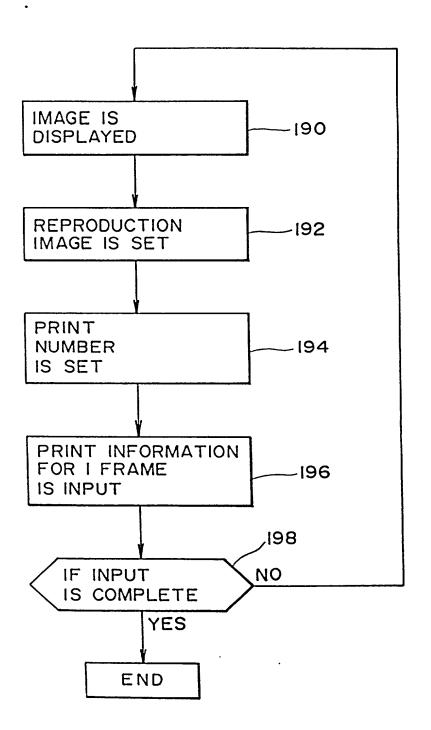
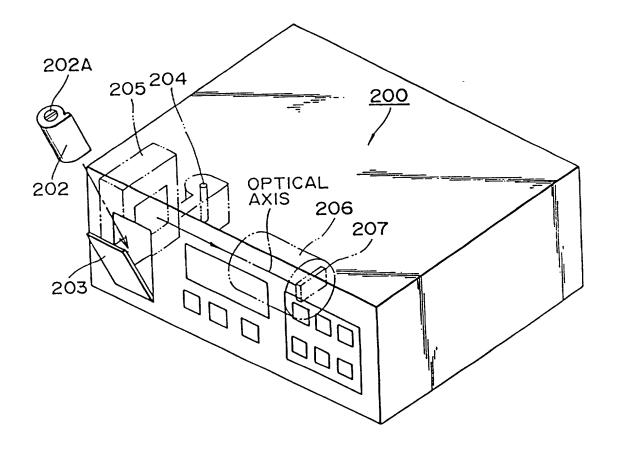
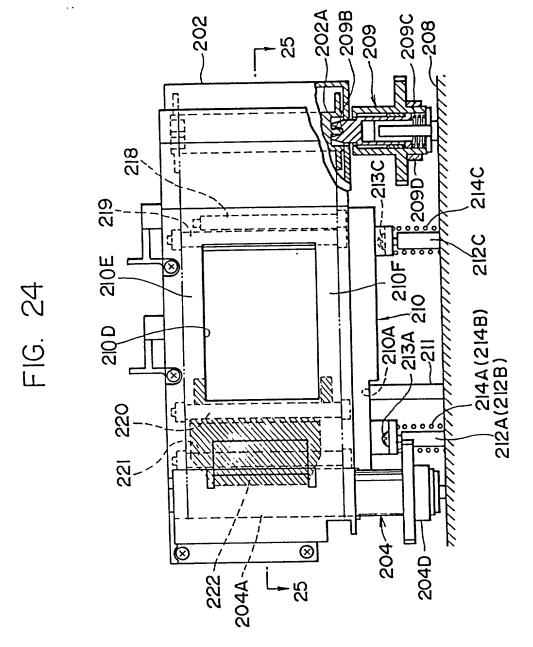


FIG. 23





-210B 213C -216 201F / 219 Kz 218 · 🕁 · 1 215\ 217 FIG. 25 <u>20</u> 210 220 2044/ 226 | 2264 213B 225A 221 (" 222 225 204B

202

FIG. 26

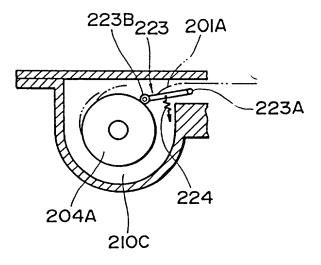


FIG. 27

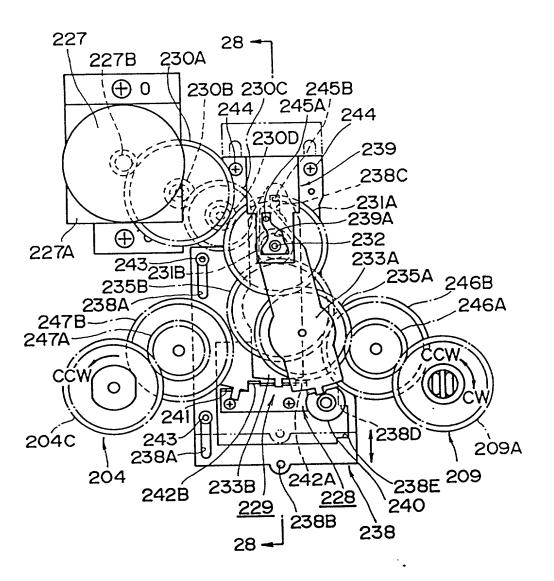


FIG. 28

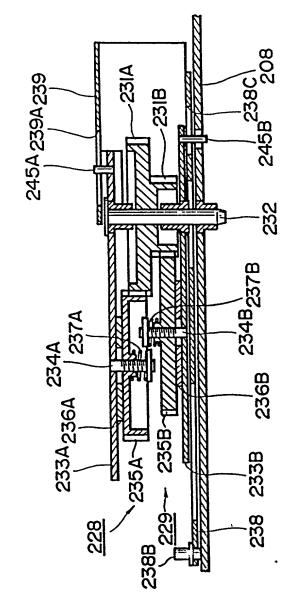


FIG. 29

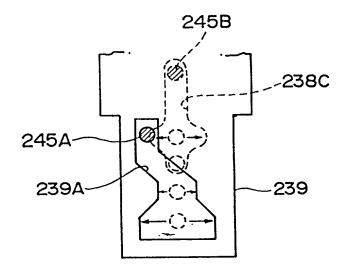


FIG. 30

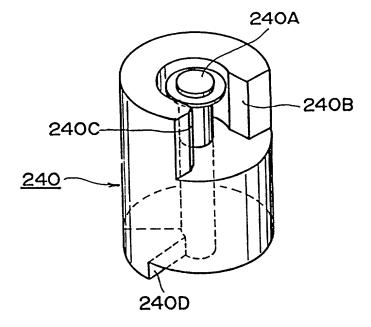


FIG. 31

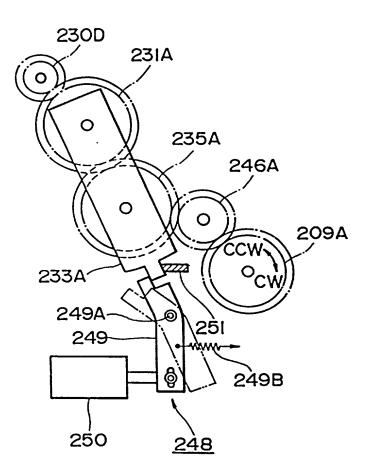


FIG. 32

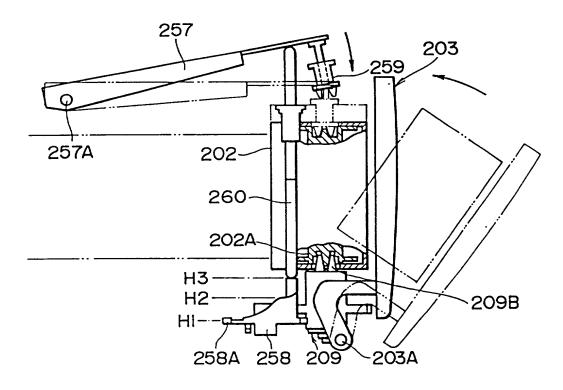


FIG. 33

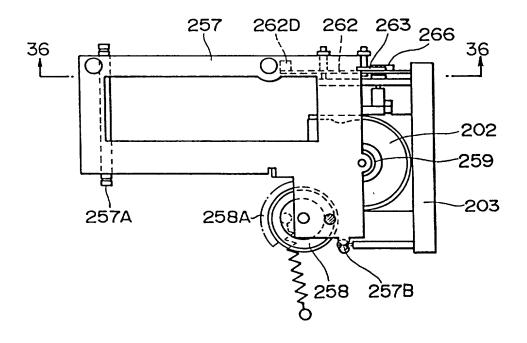


FIG. 34

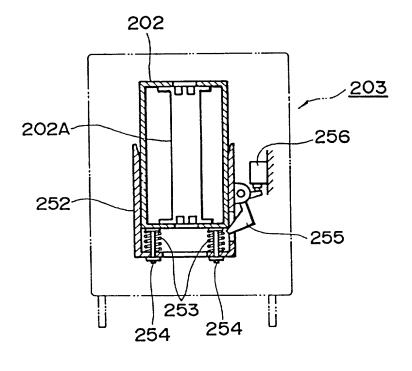
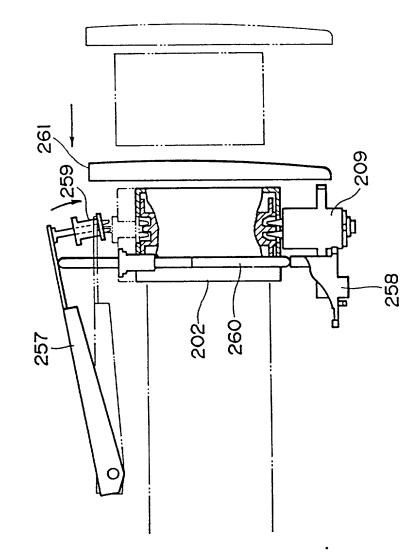
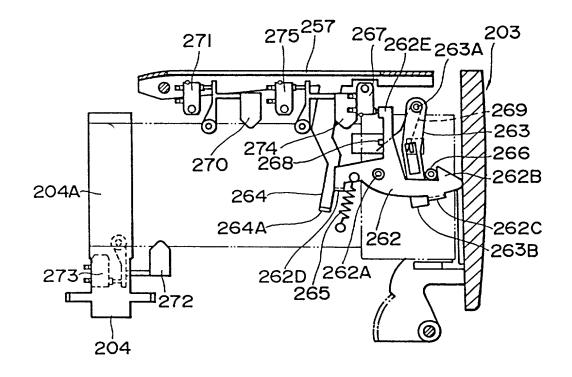


FIG. 35



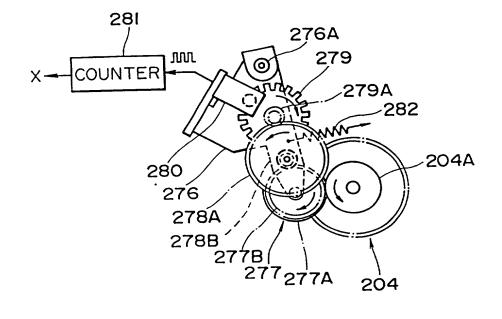
**

FIG. 36



44

FIG. 37



111.

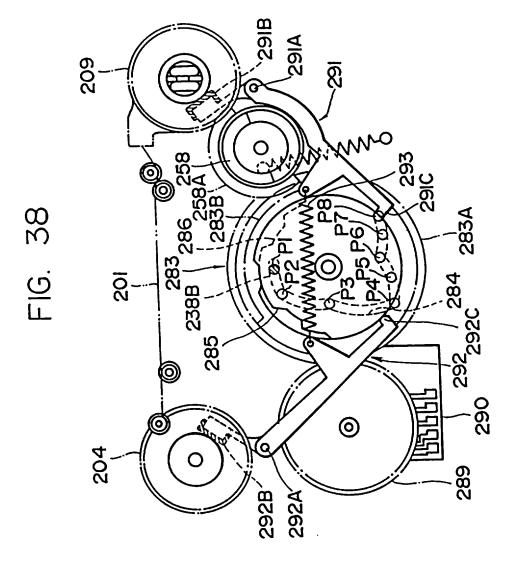
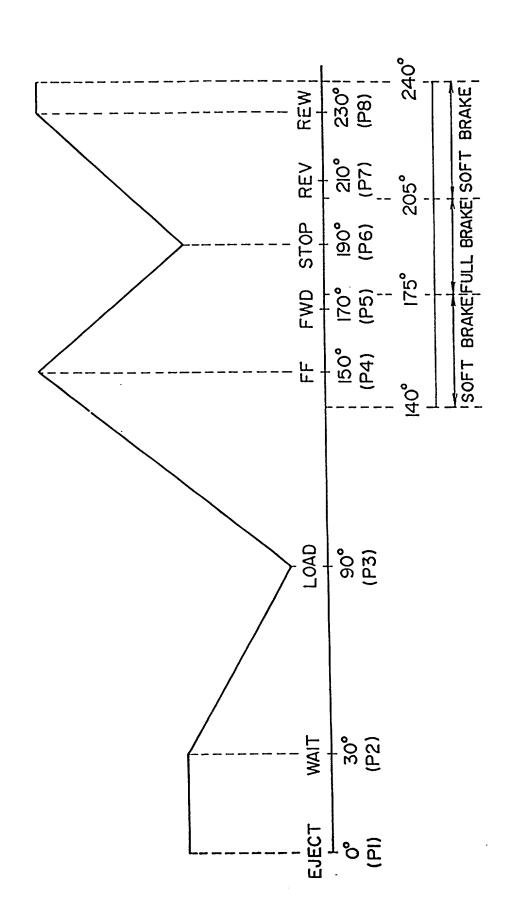
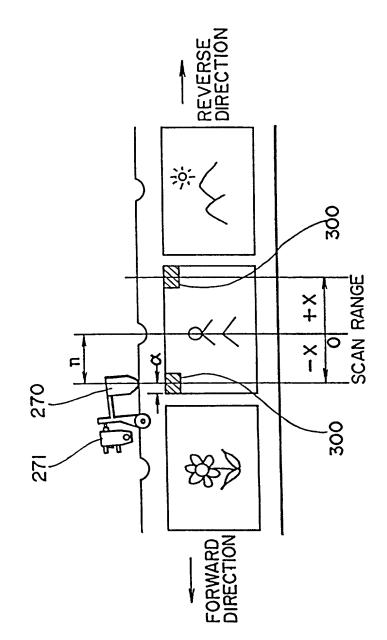


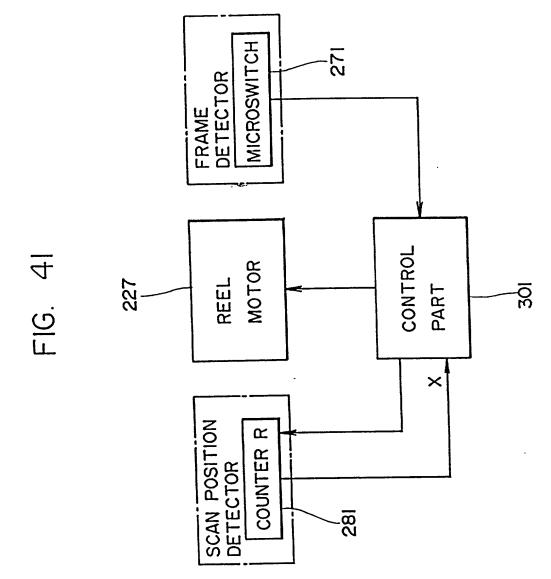
FIG. 39





F1G. 40

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1.45 11.54

FIG. 42

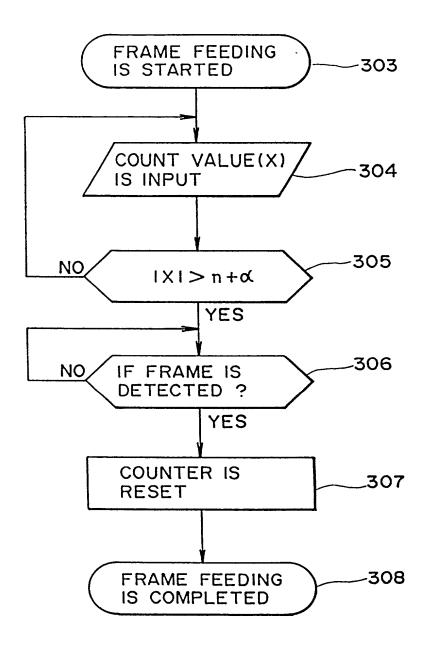


FIG. 43

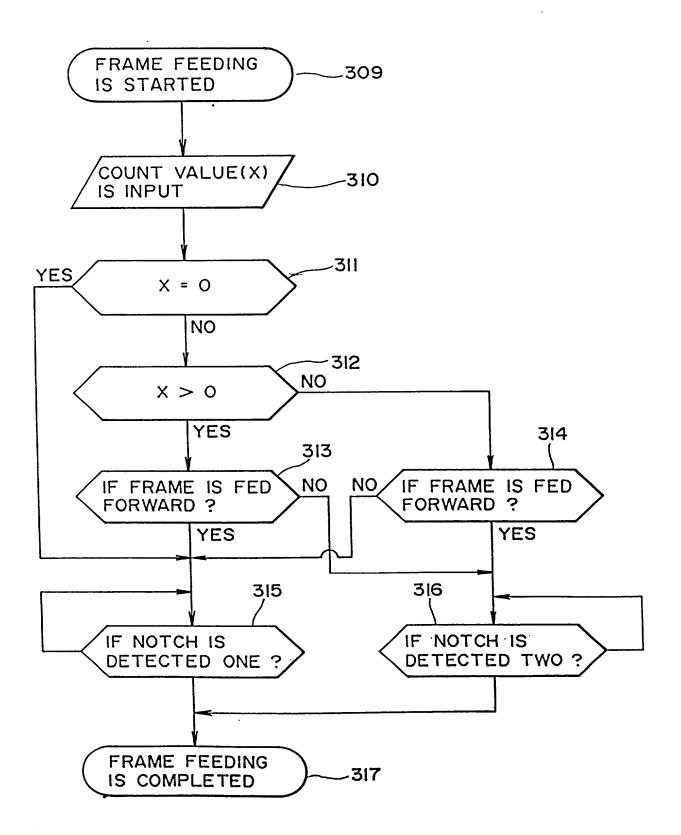


FIG. 44

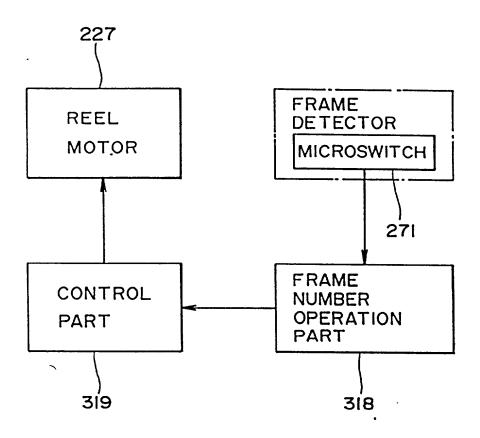


FIG. 45

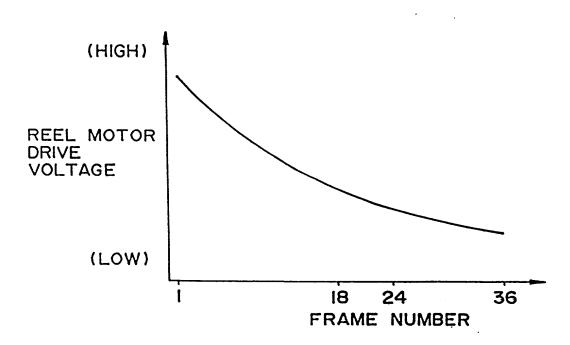
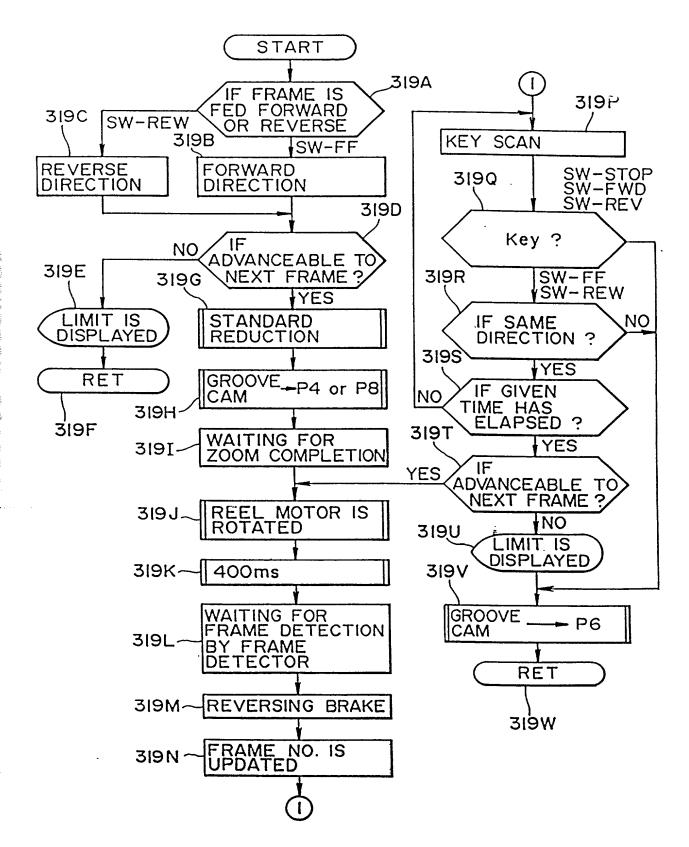


FIG. 46



(<u>(</u> IMAGE SIGNAL PROCESS CIRCUIT 323 FRAME NO. OPERATION PART NEGATIVE/ POSITIVE DECISION PART 322 48 -321 123 Q 124 124E~ 320-(E FILM DRIVE PART 134 <u>8</u>

FIG. 47

Au.

FIG. 48

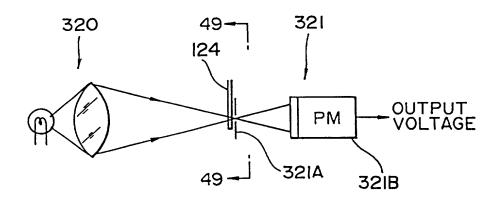


FIG. 49

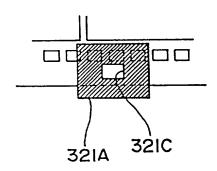


FIG. 50

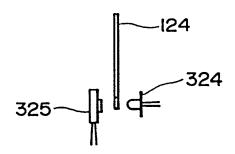


FIG. 51(A)

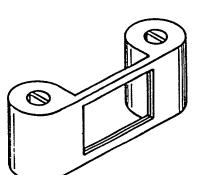
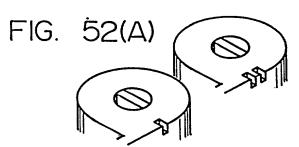


FIG. 51(B)



FIG. 52(B)



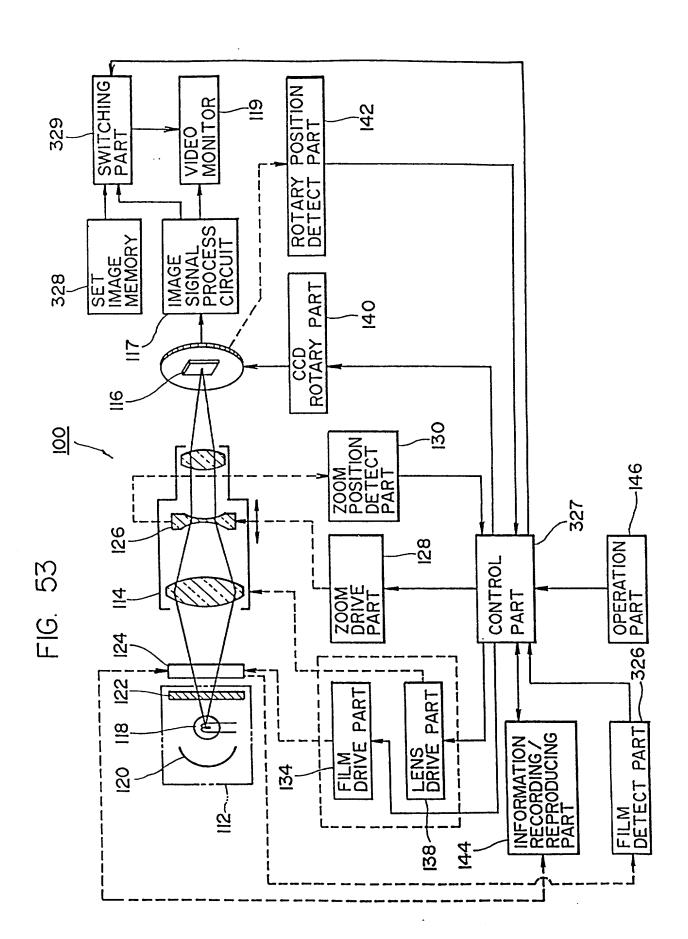


FIG. 54

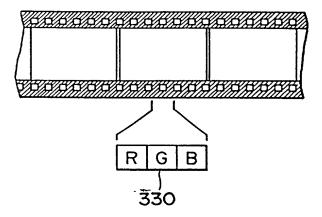
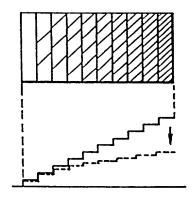
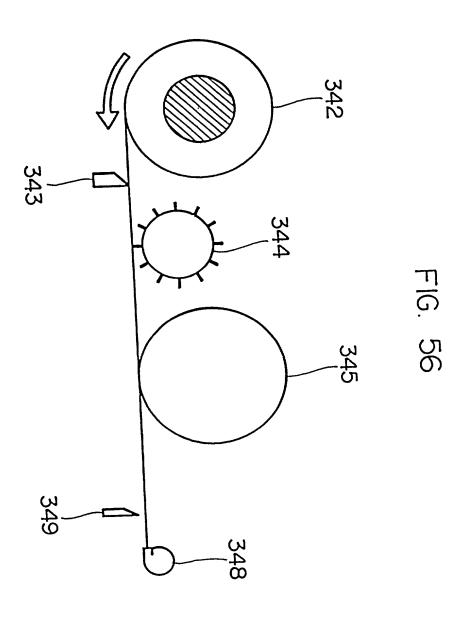


FIG. 55





<u>;</u> -

FIG. 57

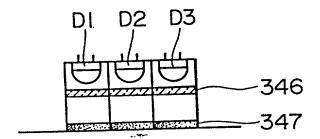


FIG. 58

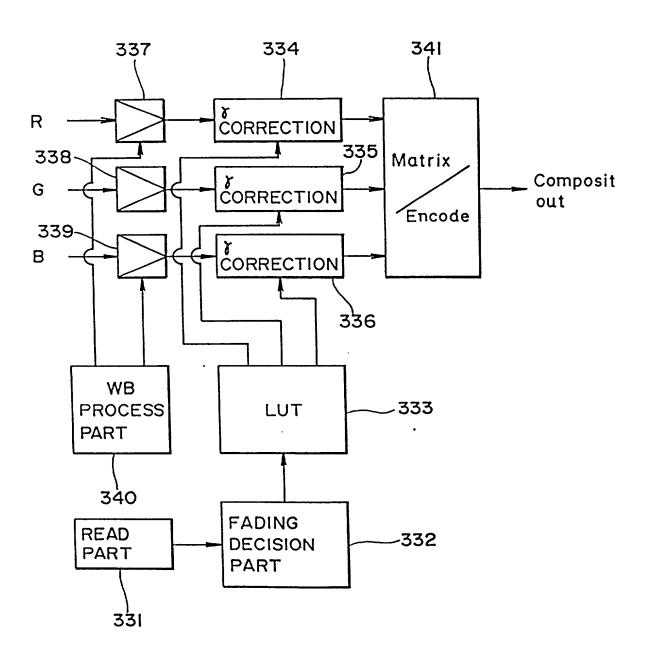


FIG. 59

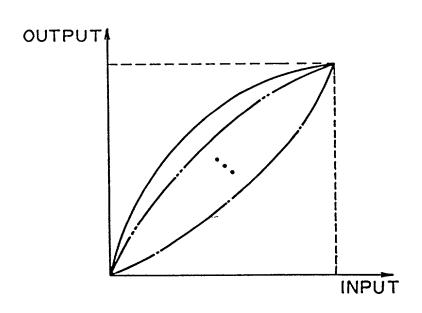


FIG. 60

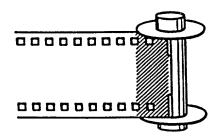


FIG. 61

PRIOR ART

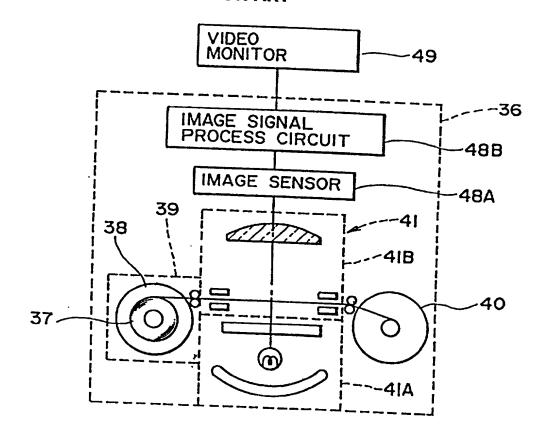
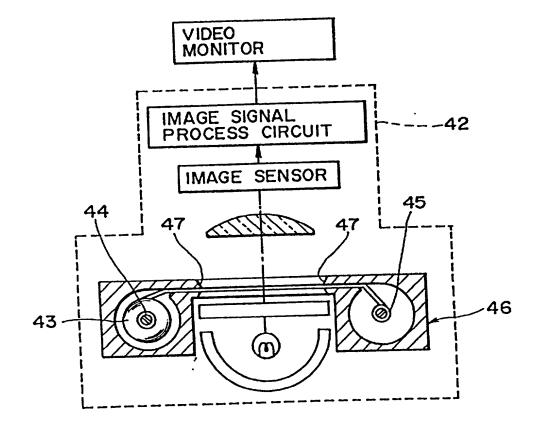


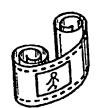
FIG. 62

PRIOR ART



PRIOR ART

FIG. 63(A)



PRIOR ART

FIG. 63(B)

